



Utility Patent Application

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re t	he Patent Application of:	
	Khare, et al.	) )
Serial	No.: 09/752,534	) ) Examiner: Patel, Hetul B.
Assignee: Intel Corporation )		) )
Filed:	December 29, 2000	) ) )    Art Unit:  2186
For:	A MECHANISM FOR EFFICIENTLY SUPPORTING THE FULL MESI (MODIFIED,	) Alt Gliit. 2100 )
	EXCLUSIVE, SHARED, INVALID) PROTOCOL IN A CACHE COHERENT MULTI-NODE	RECEIVED
	SHARED MEMORY SYSTEM	JAN 07 2004
RESPONSE TO OFFICE ACTION		

Technology Cellle

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that I am causing the above-referenced correspondence to be deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and that this paper or fee has been addressed to the Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: \_ Debbie Peloquin Name of Person Mailing Correspondence: December 31, 2003 December 31, 2003 Date Signature

Docket No.: 42390P9878 Application No.: 09/752,534

## **INTRODUCTORY COMMENTS**

Sir:

In response to the Office Action mailed October 1, 2003, Applicants respectfully request the Examiner to enter the following amendment and to consider the following Remarks. A newly executed Oath/Declaration also accompanies this Response.

Docket No.: 42390P9878 Application No.: 09/752,534